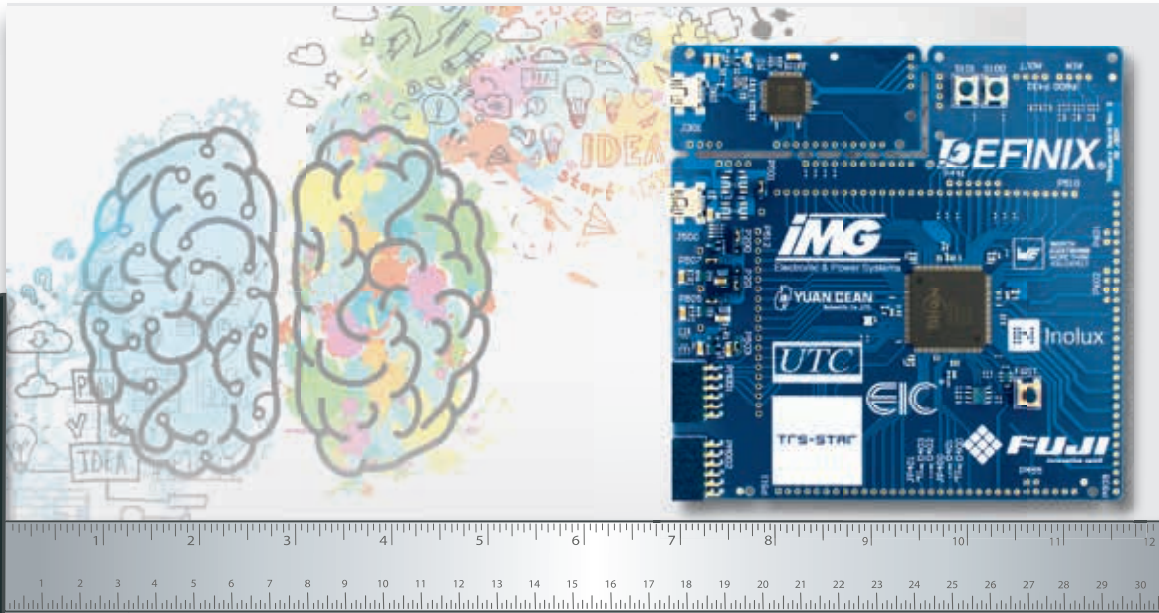


TFS-STAR

Be creative with T*Square Boards

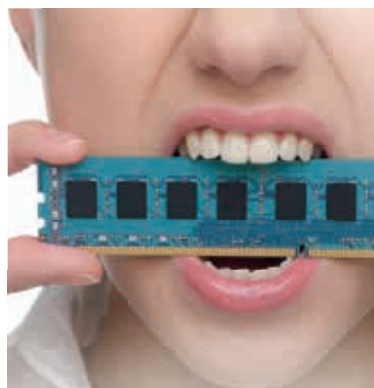


TFS-STAR

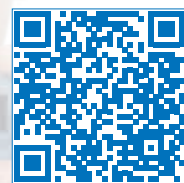
IMG
Electronic & Power Systems

- Rapid Prototyping
- Measure power consumption on all voltage rails
- RISC-V [Sapphire] evaluation
- UART to RISC-V
- Evaluation of ADCs/DACs in a digital FPGA
- Break-Out-On-Board-Programmer

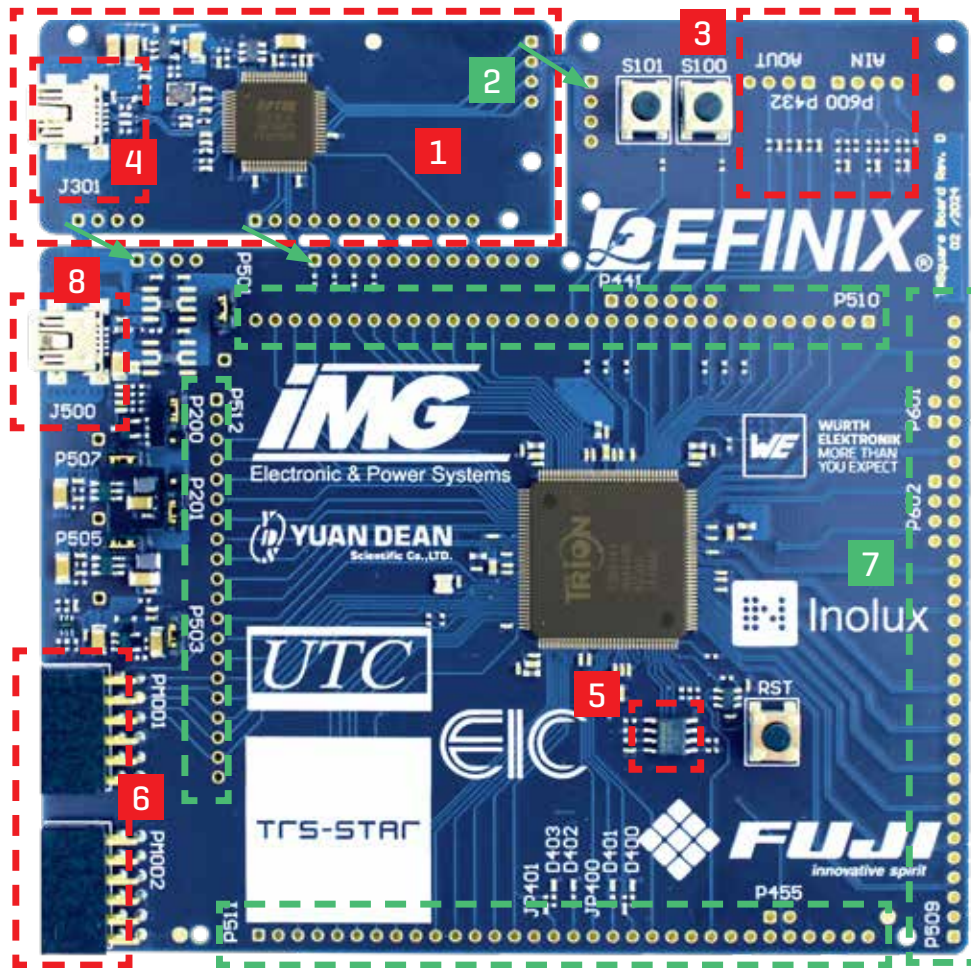
WEBINAR INVITATION



TEC BYTES



Bite-sized
technology topics



- 1** Break-Out-Board with Programmer Break-Out to use it for your own boards
- 2** There is still the option to continue to use the programmer in combination with the T*Square Education Board
- 3** External R-C-circuitry allows implementation of ADCs and DACs in a digital FPGA, using LVDS-Buffers for sigma-delta-ADCs
- 4** A UART allows communication with the RISC-V Sapphire Soft-Core that can be implemented in the FPGA Core fabric
- 5** The PCB allows to mount either 100-pin LQFP [with integrated SPI-Flash] or 144-pin LQFP [with external SPI-Flash]
- 6** 2 x PMOD interfaces allow attaching PMOD daughter cards
- 7** Post connectors [included, but not mounted] can be mounted on the top or bottom of the T*Square education board to connect your own basedboards or daughter cards
- 8** Alternative USB-Connector for Board Power Supply, when Break-Out-Programmer [1] is removed

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